

## U.S. UTILITY Patent Application

<b>O.I.P.E.</b> AD SCANNED <b>IKB</b> G.A. <b>TM</b>	<b>PATENT DATE</b> _____
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APPLICATION NO. 09/898832	CONT/PRIOR CROSS	CLASS 21712	SUBCLASS 205	ART UNIT 283 <del>2000</del>	EXAMINER Chan, Eddie
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**APPLICANTS**

Thomas McDonald

234

**title**

Apparatus and method for selectively accessing disparate  
instruction buffer stages based on branch target address cache hit  
and instruction stage wrap

PTO-2040  
12/89

**PTO-2040**  
**12/89**

**ISSUING CLASSIFICATION**[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____  _____  _____  _____ (Primary Examiner) (Date)			
<b>ISSUE FEE</b>				
Amount Due			Date Paid	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	

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